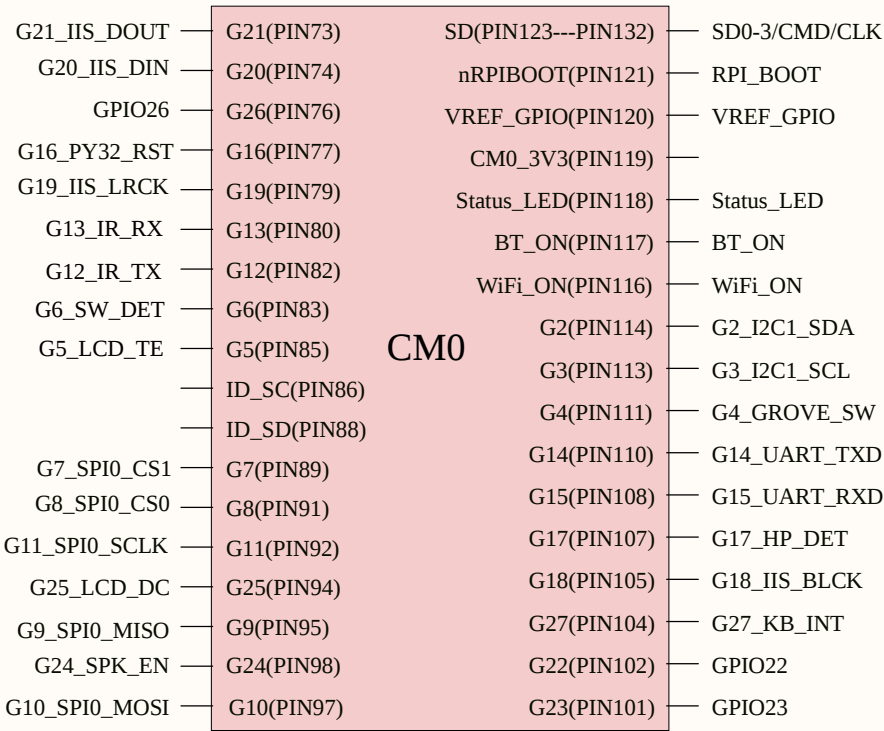
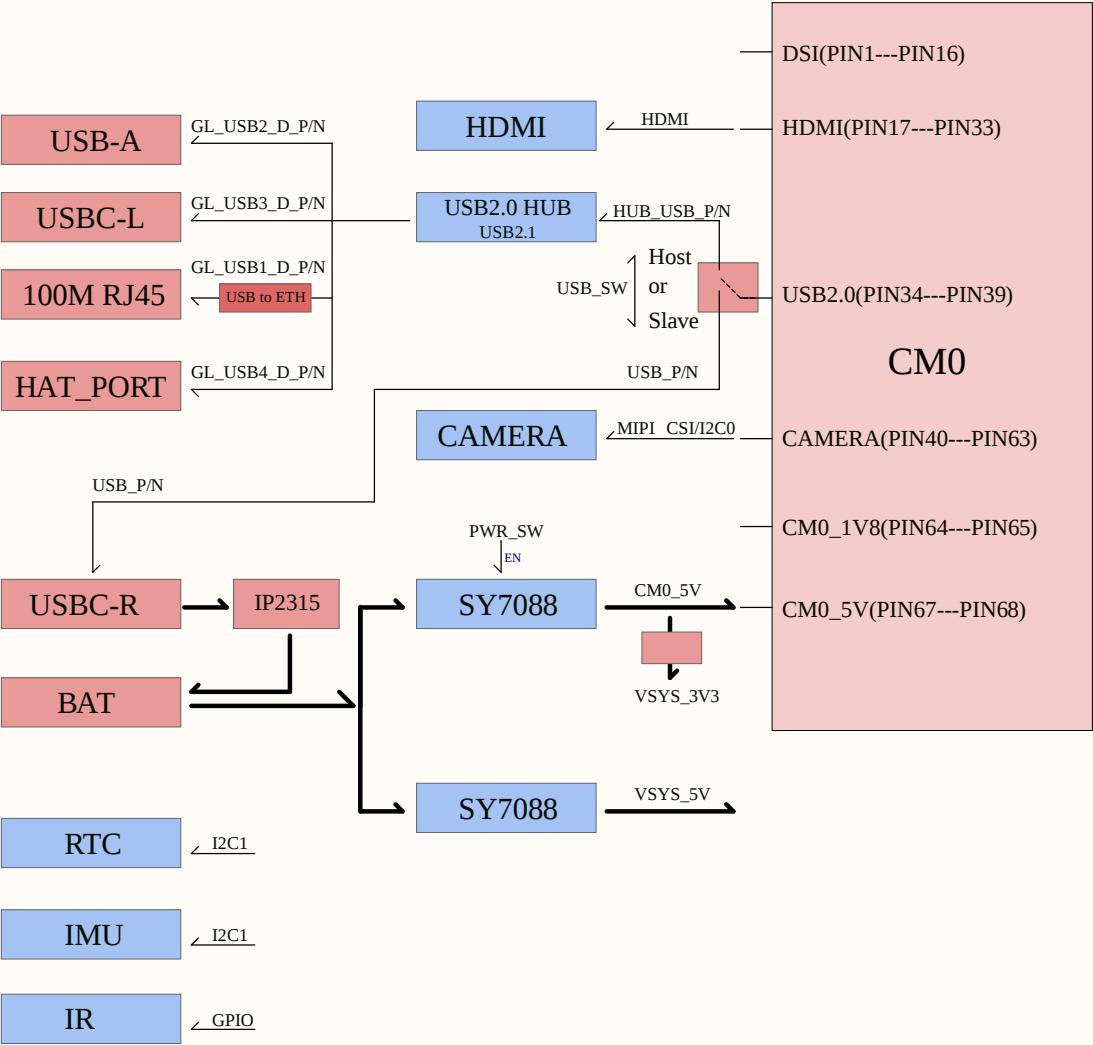
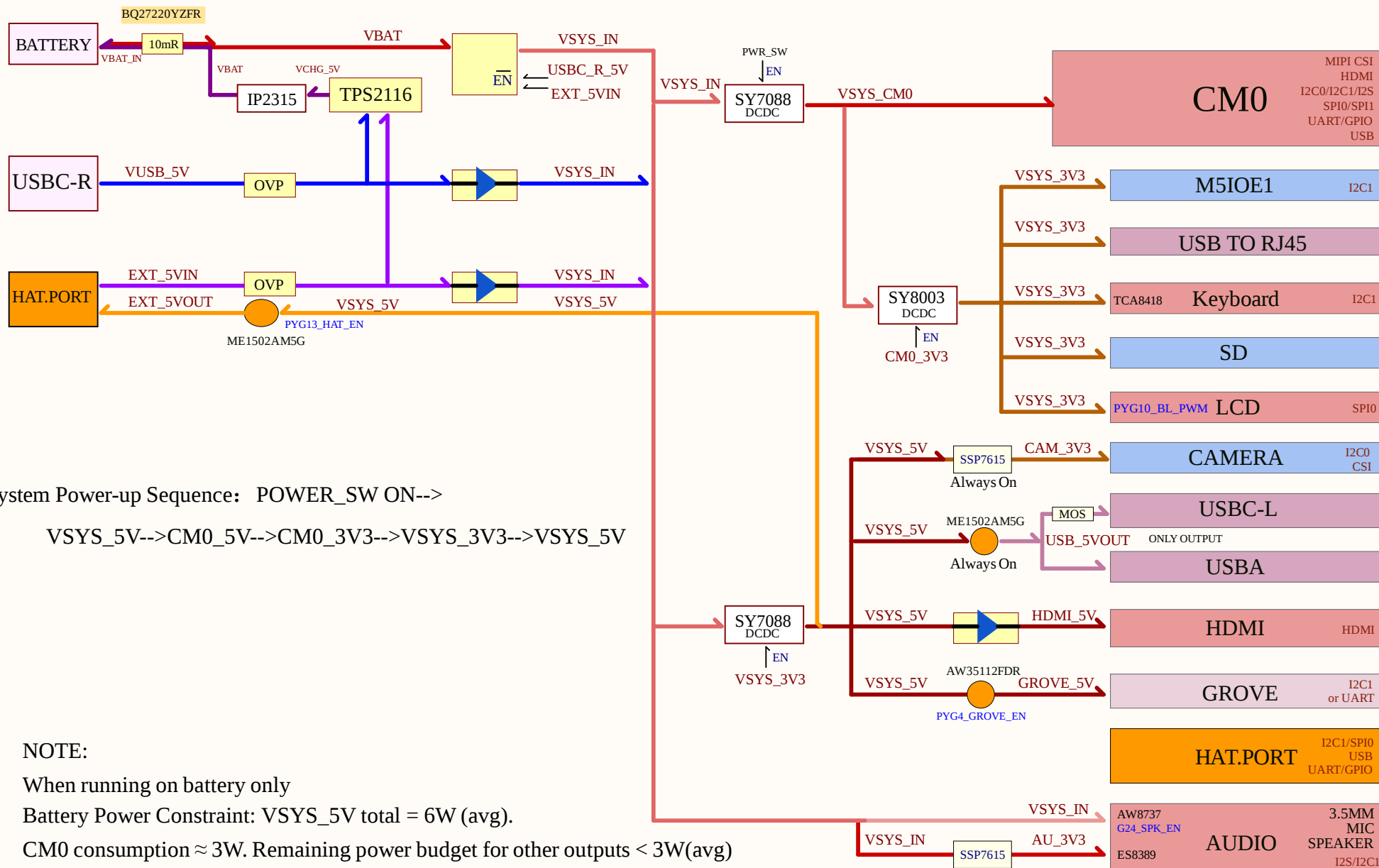


Block diagram



Power Network

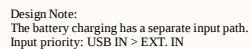


A



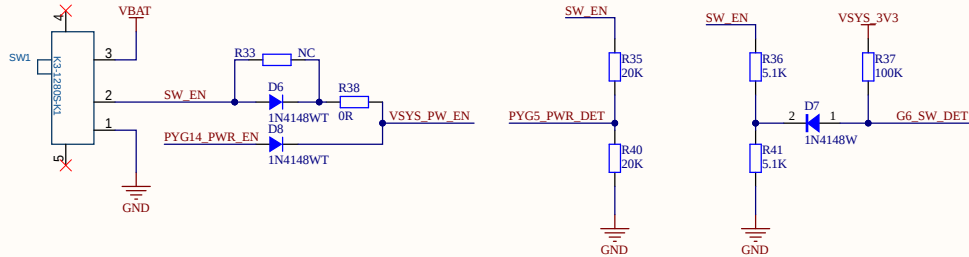
C

D

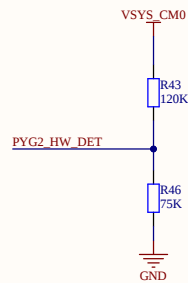


SWITCH

PWR_SW

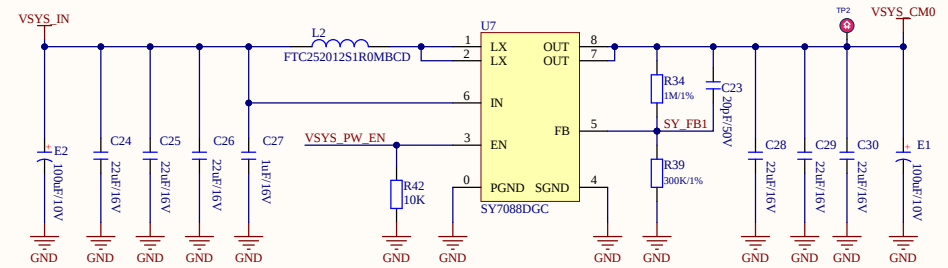
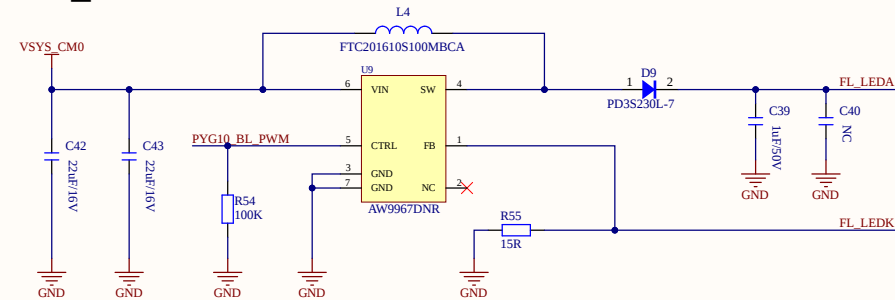


Hardware Version

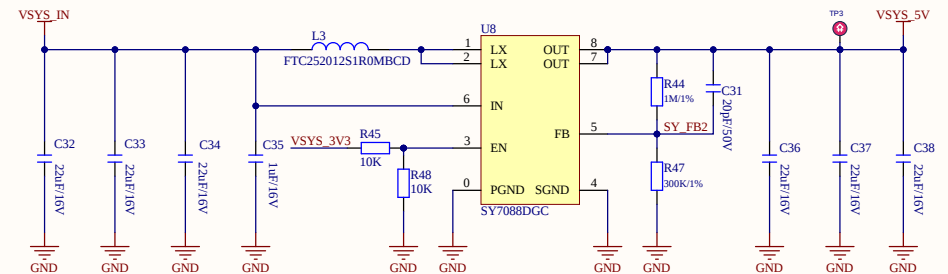


Hardware Version	
PYG2_HW_DET	HW_Version
2.5V	V0.3 For internal use only
2.0V	V0.6
1.5V	
1.0V	
0.5V	
0V	

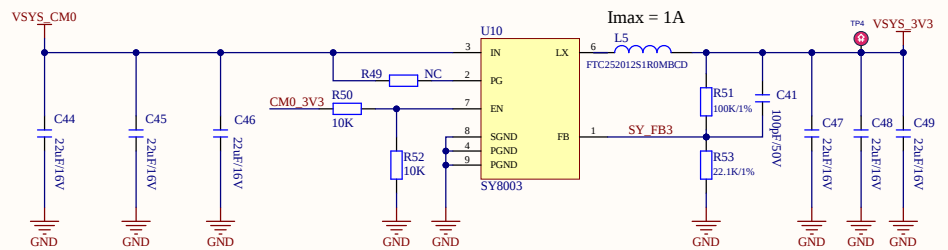
LCD_BL



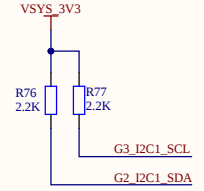
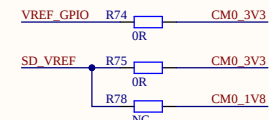
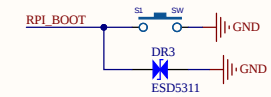
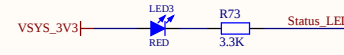
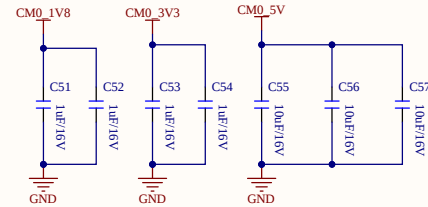
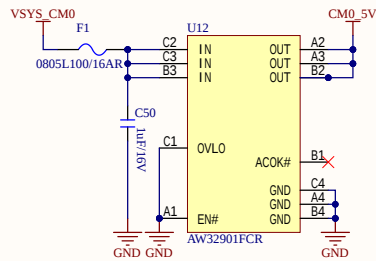
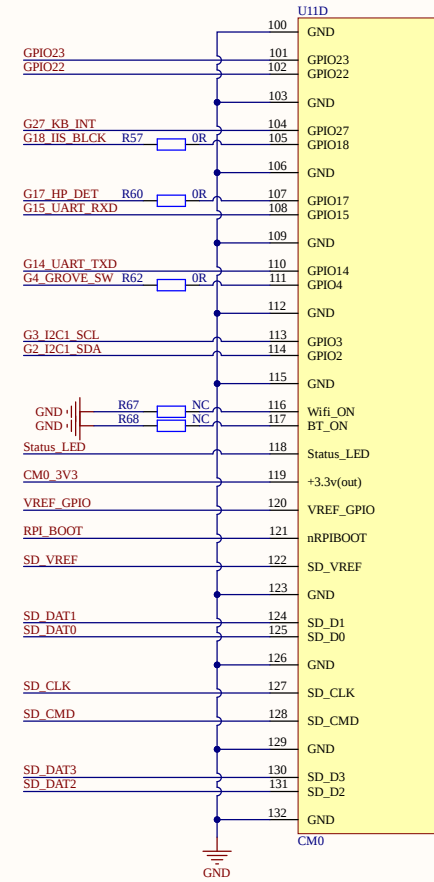
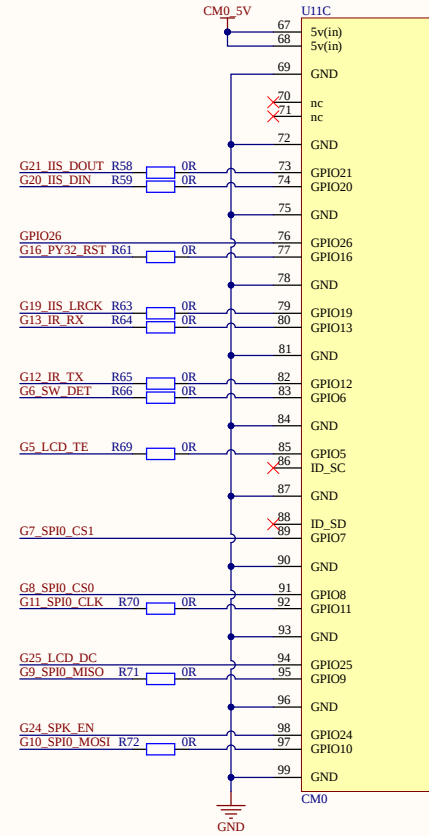
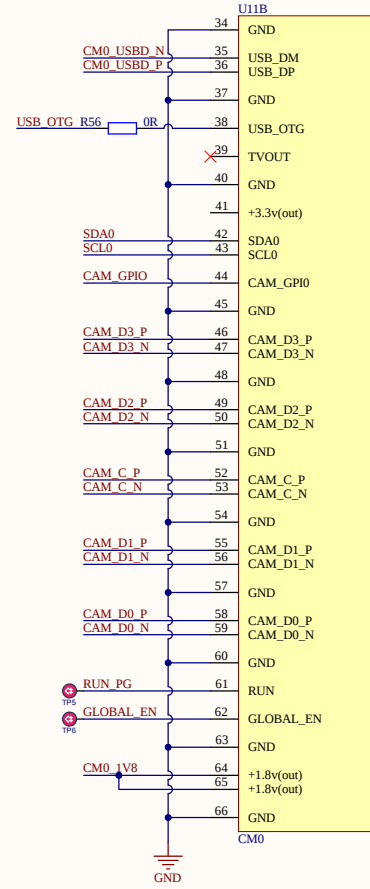
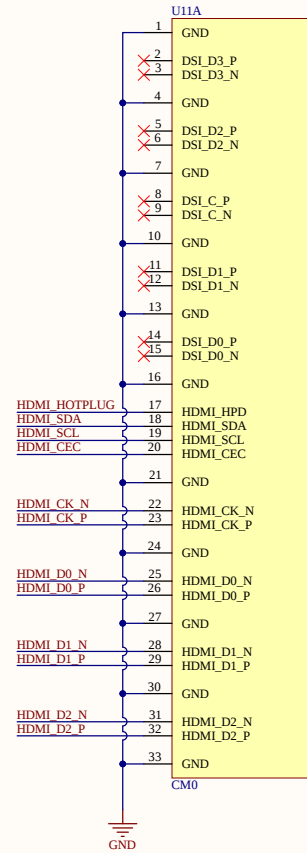
$V_{out} = 1.2(1 + (R15/R17)) = 5.2V$



$V_{out} = 1.2(1 + (R15/R17)) = 5.2V$



05 CM0

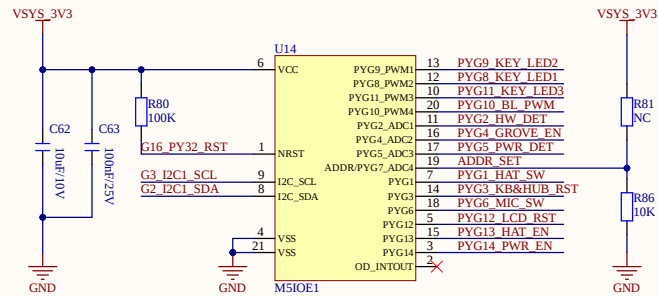


Note: SDVREF must be 1.8V when using eMMC.



06 Peripheral

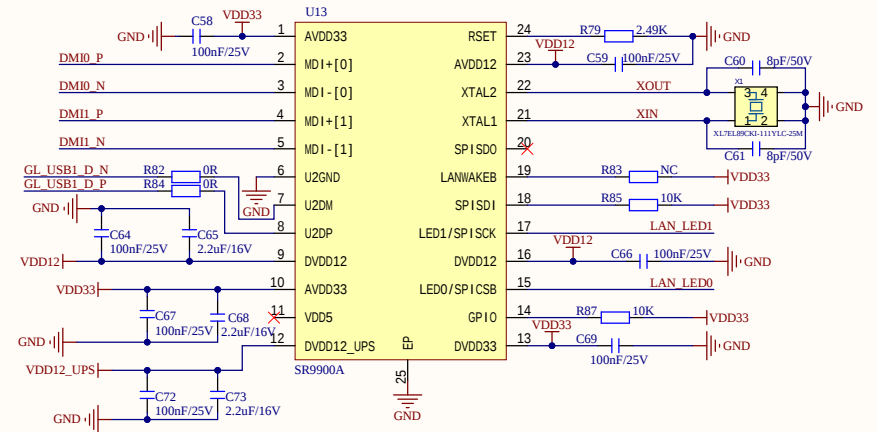
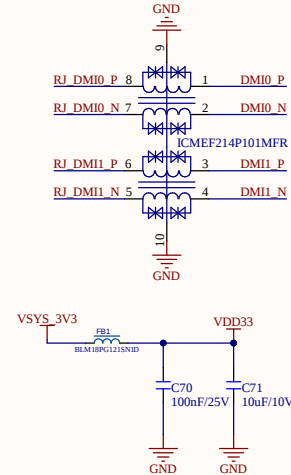
M5IOM1



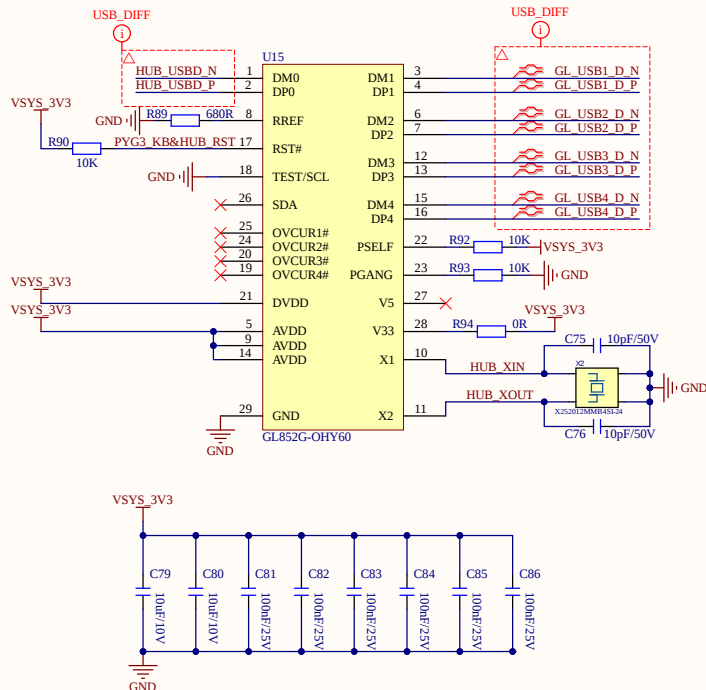
IIC Address:0x4F

PYG13_HAT_EN SWC TP7
PYG12_LCD_RST SWD TP8
G16_PYG3_RST TP9

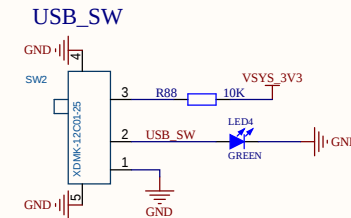
USB TO LAN 100M



USB 2.0 HUB



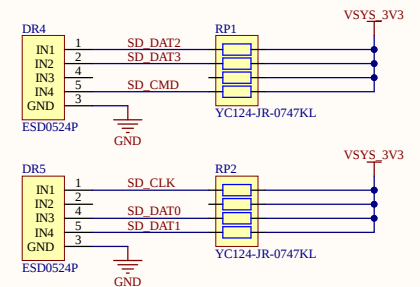
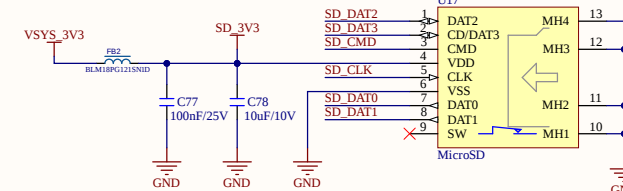
USB HOST & Device



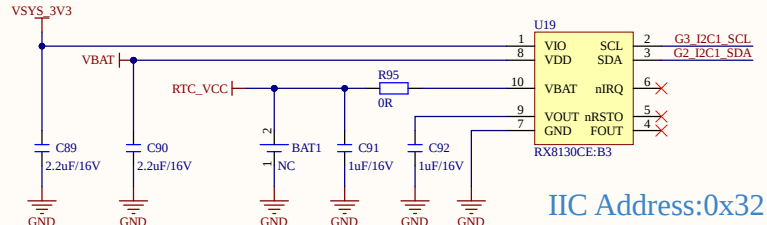
Pulling the USB_OTG pin low switches the CM0 to USB Host mode.
Green LED ON: Zero operates as Host.
Green LED OFF: Zero operates as Device.

USB_SW	Function
L	USBC2_D_P/N
H	HUB_USBD_P/N

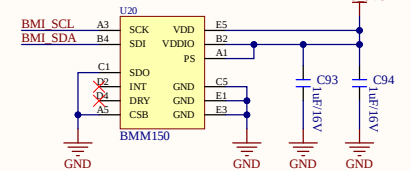
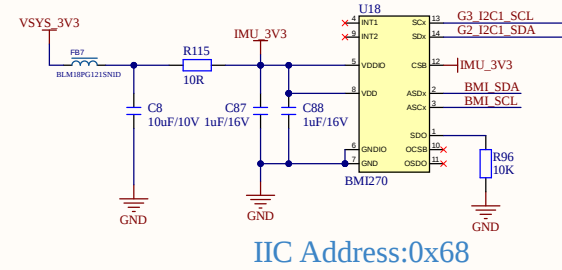
SD



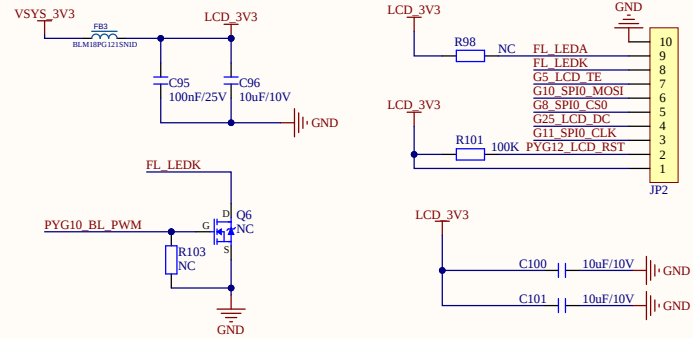
RTC



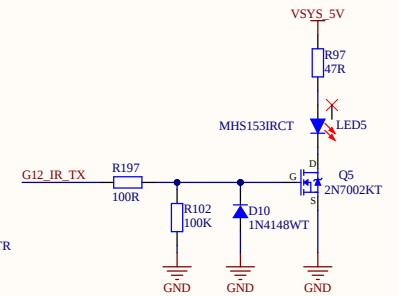
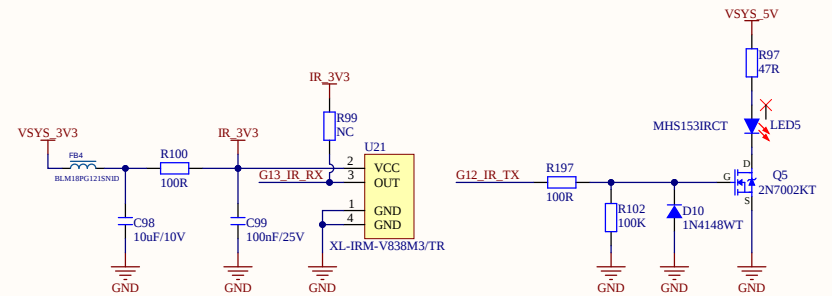
IMU



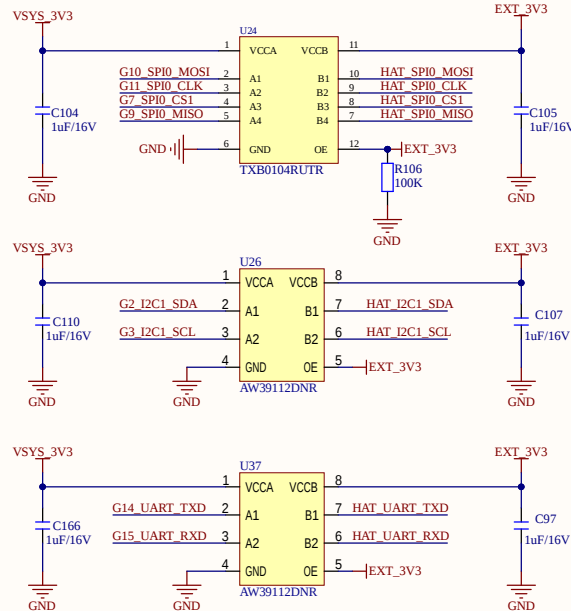
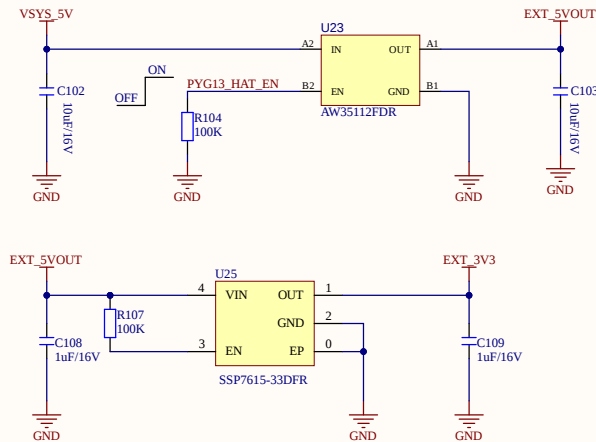
LCD



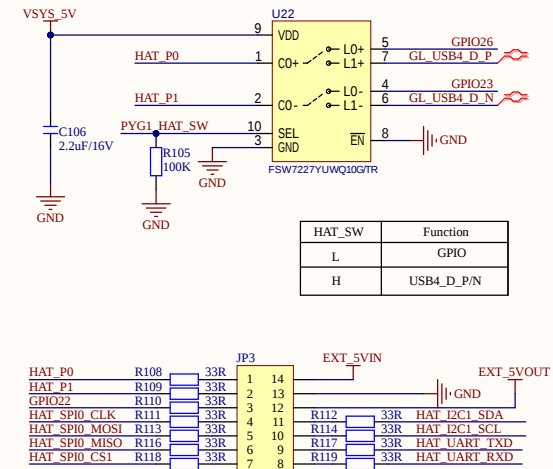
IR



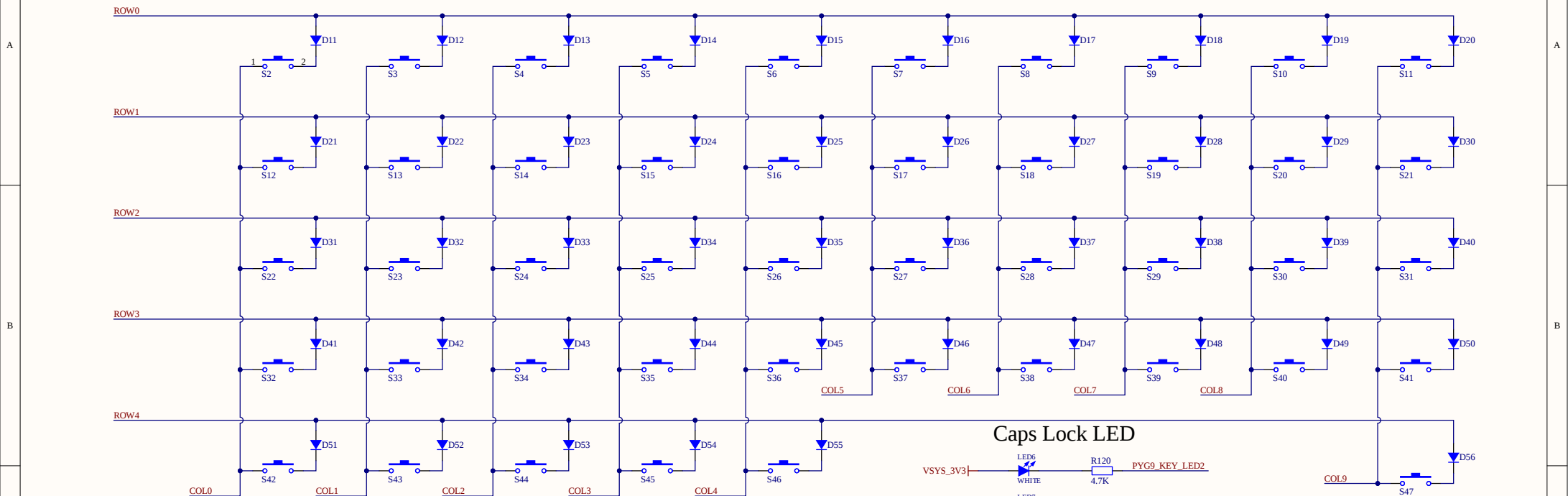
HAT.PORT



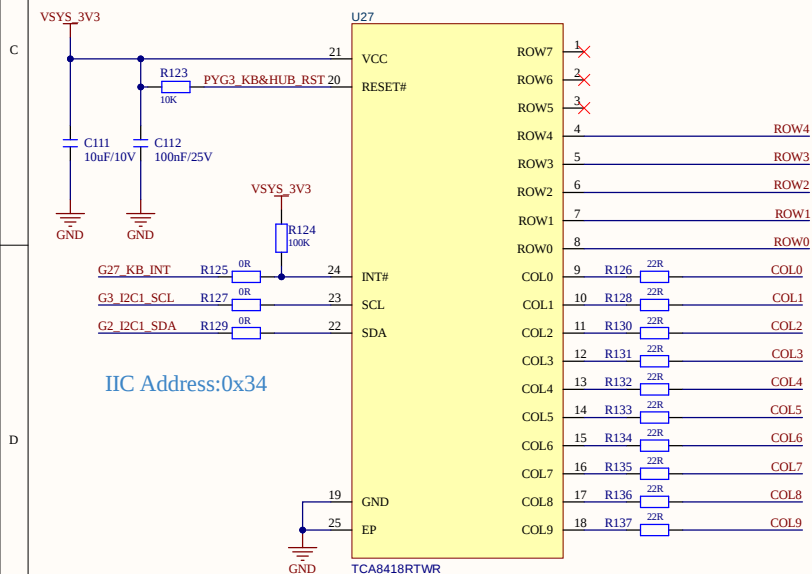
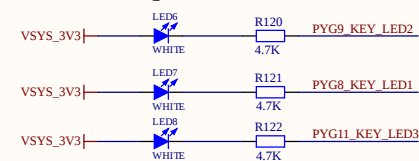
EXT.HAT-USB



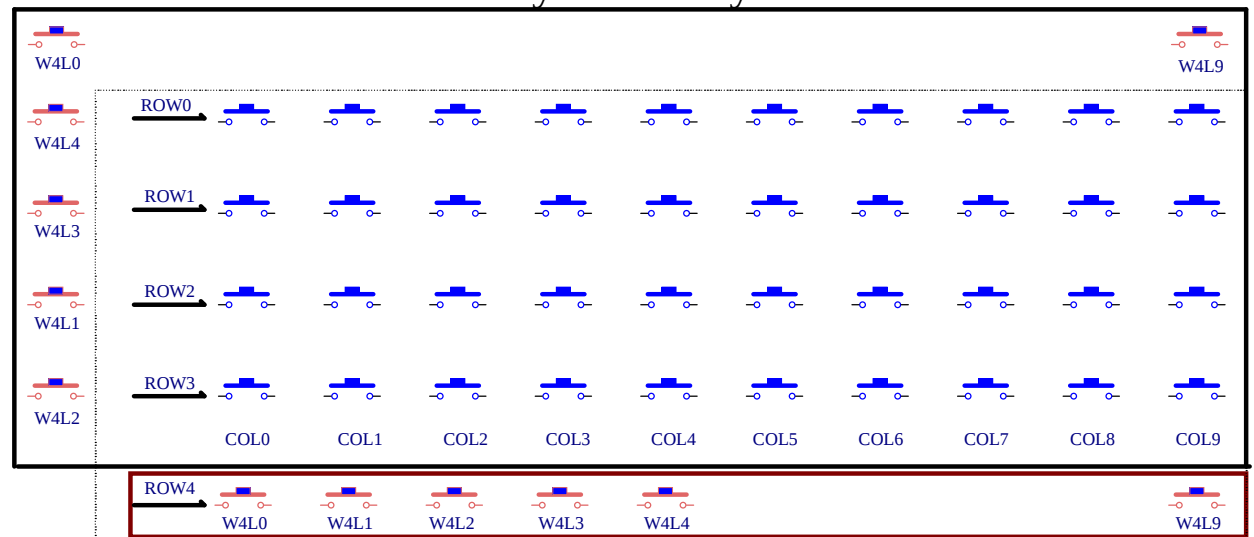
07 Keyboard



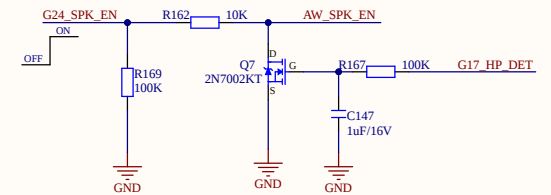
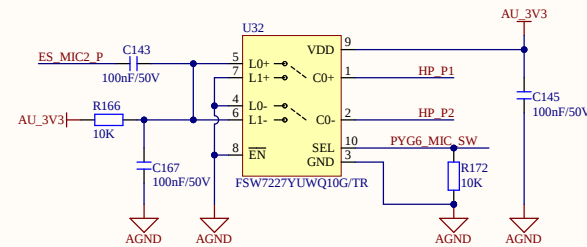
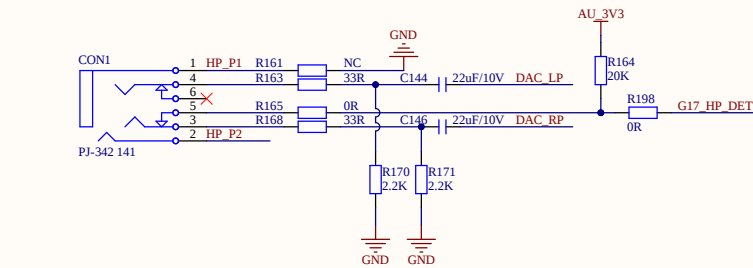
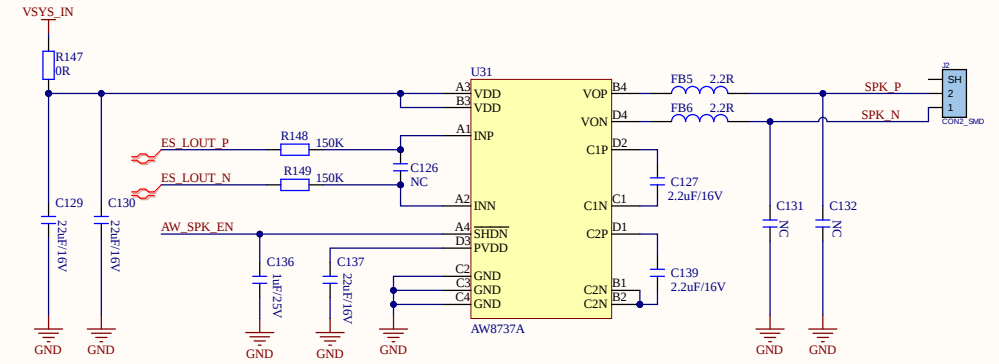
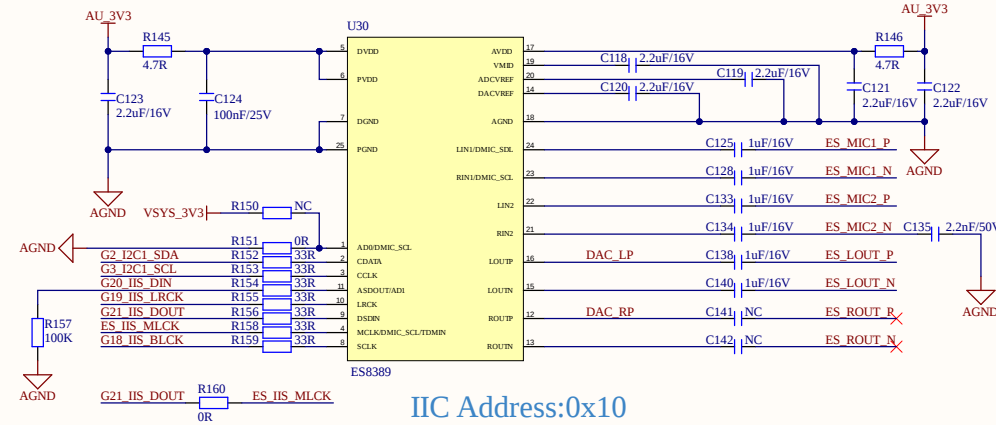
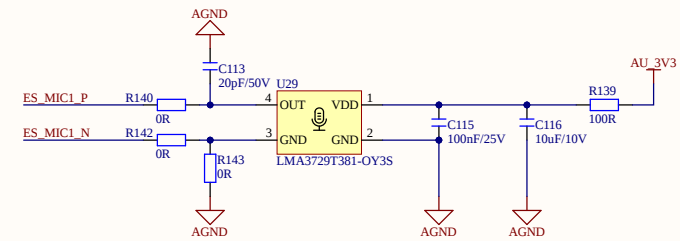
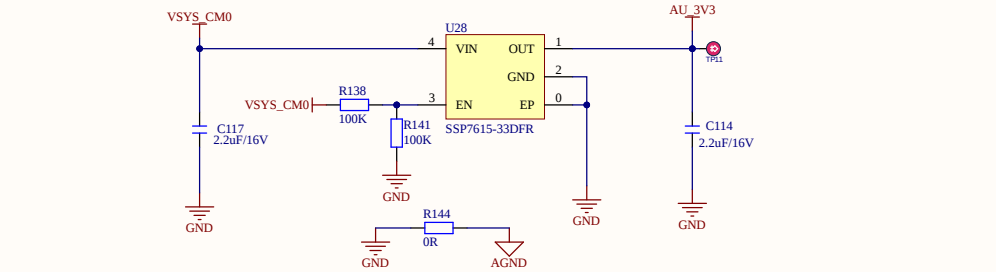
Caps Lock LED



Keyboard Layout

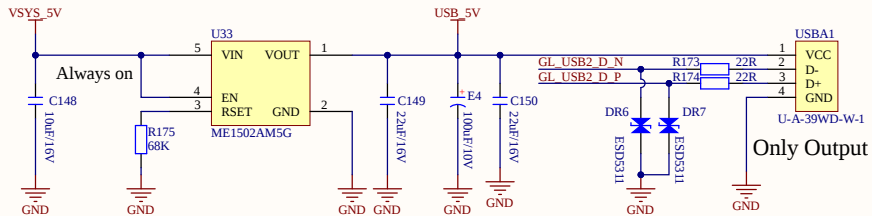


AUDIO CODEC

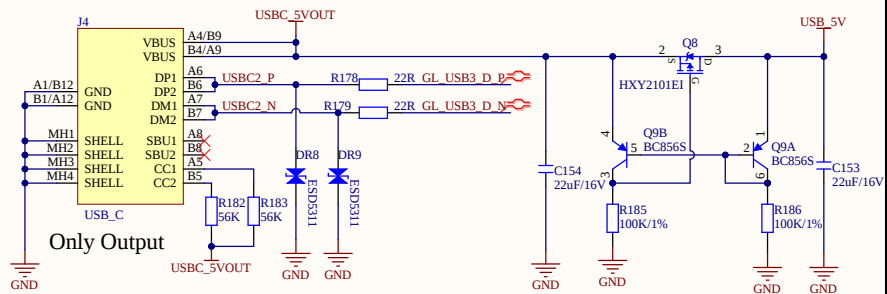


PYG6_MIC_SW : OMTP and CTIA

USBC_L



USBA+USBC_L : Current Limit: 1A



USBA

RJ45

